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SERIAL NUMBER FILING or 371(c)

CONFIRMATION NO. 9982

10/568,50	1	02/16/2			414		3652		2006-0163A	
	RULE									
APPLICANTS Akira Kabeshita, Osaka, JAPAN; Kurayasu Hamasaki, Osaka, JAPAN; Shozo Minarnitani, Osaka, JAPAN; Yoichi Makino, Fukuoka, JAPAN; Noriyuki Tani, Fukuoka, JAPAN; ***CONTINUING DATA***********************************										
This application is a 371 of PCT/JP04/13281 09/07/2004 ** FOREIGN APPLICATIONS ************************************										
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 07/21/2006										
Foreign Priority claims 35 USC 119(a-d) cond Verified and	ditions met		☐ Met af Allowa	ter ince	STATE OR COUNTRY		HEETS	TOTA CLAIM		INDEPENDENT CLAIMS
Acknowledged	CHARLES Examiner's	Signature	Initials		JAPAN		7	19		2
ADDRESS WENDEROTH, LIND & PONACK L.L.P. 1030 15th Street, N.W. Suite 400 East Washington, DC 20005-1503 UNITED STATES										
TITLE										
Apparatus and method for removing semiconductor chip										
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT						☐ All Fees			
FILING FEE							1.16 Fees (Filing)			
RECEIVED							1.17 Fees (Processing Ext. of time)			
900	No for following:					1.18 Fees (Issue)				
						Other				
☐ Credit										

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